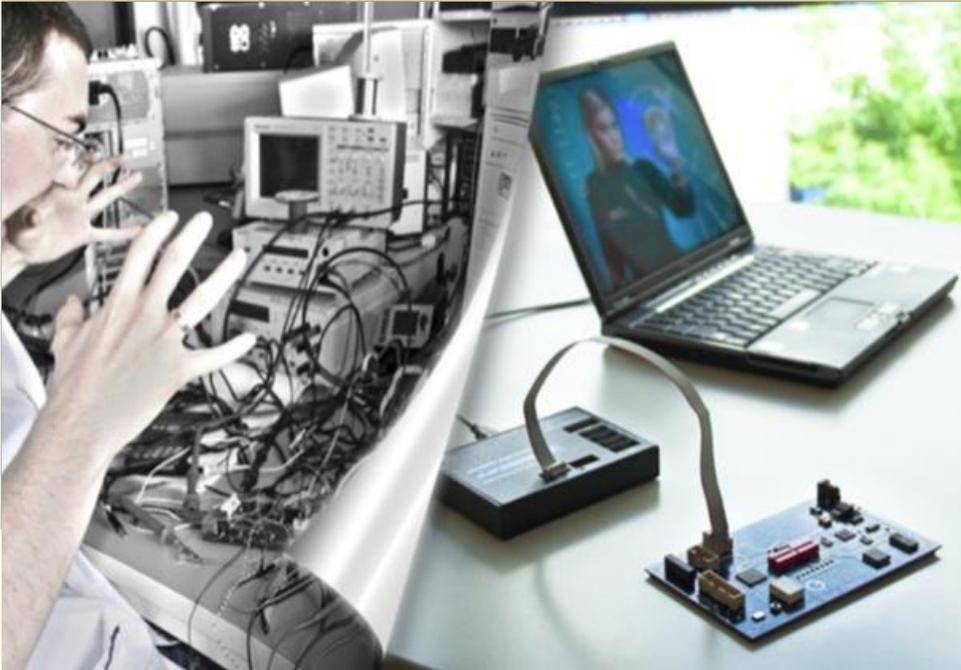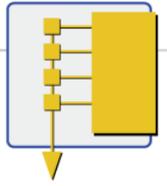# Embedded Board Test (JTAG/Boundary Scan+)
1-day Introductory Training

Disrupt the traditional functional board test!

Reduce your costs!

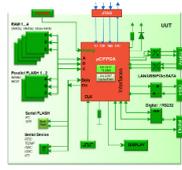Accelerate the development of new products!

Improve the quality of your products!

Reduce time to market!
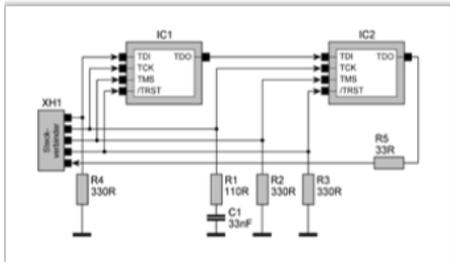
How – with Embedded Board Test!

DSE TEST SOLUTIONS

EPOTeQ – Solutions for Electronic Production

Embedded Test 1-day Introductory Training

TEST CAPABILITIES

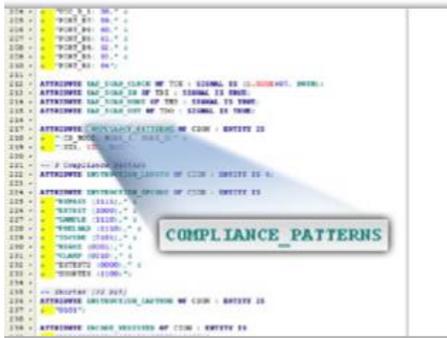Can you test a board with **only one** central JTAG/ Boundary Scan device? (Even no JTAG/Boundary Scan?)

Many new designs are based on an MCU, CPU, DSP or an FPGA which typically has a **test port** (JTAG - or SWD, BDM…).

Cover the blocks in green with Embedded Board Test Solutions – Stand-alone or smoothly integrated also with NI and other test system suppliers.

Shift chain layout

COMPLIANCE_PATTERNS

Excerpt from a BSDL file

How would you like a test method that….?

- ✓ Requires no or just a few specific test points
- ✓ Simplifies board level functional testing
- ✓ Seriously improves diagnostics/troubleshooting
- ✓ Reduces total test time dramatically (less systems needed)
- ✓ Provides a significant better test economy

These are some of the many benefits of Embedded Board Test!

DSE and **EP-TeQ** now give you the opportunity of learning more about this technology at our **Embedded Board Test 1-day Training** (2- or 3-days trainings available, too).

You can also choose a **Starter Kit** which includes hardware, software and basic training for 2 or 3 days.

Training contents

- · Introduction to Boundary Scan and Embedded Board Test
- · Design for Test (DfT)
- · Debug and verification of prototypes
- · Test and programming of prototype boards and NPI
- · Reuse of tests in manufacturing testing

This 1-day training is intended for **hardware developers**.
The training material is in English, but the training may be held in English or in Danish.

Dates - please contact DSE or EP-TeQ (see below).

Price per person
DKK 4.500,- excl. VAT (incl. training material & meals)

Participants
4-6

Locations

EP-TeQ A/S	DSE Test Solutions A/S
Niels Bohrs Vej 32, Stilling	Sverigesvej 19
8660 Skanderborg	8700 Horsens

General information
Registration is final and on a 'first-come' basis.

For registration and questions, please contact:

Mette Søgaard Andersen	Toni Arnth Bojsen
Mobile: 20983731	Mobile: 25101047
Mail: msa@ep-teq.com	Mail: tab@dse.dk

About the instructors

Henrik Jensen, DSE:
Henrik has profound qualifications within Embedded Board Test having worked for many years with customer applications and training. As DSE project manager, he is today in charge of the customer projects based on Embedded Board Test.

Sten Iversen, EP-TeQ:
Sten is the EP-TeQ expert in Design-for-Test (DfT) and Embedded Board Test. With his broad qualifications and long experience with hardware development from Ericsson DIAX, he takes care of support and training of EP-TeQ's many customers.